



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Pochiraju Srinivas Rao

Serial No: 09/990,841

Filed: November 9, 2001

For: Sub-Banding of Display List and Video Buffer for Page Rendering in a Digital
Signal Processor

TI-31068

Art Unit: 2853

Examiner:

Conf. No.: 2705

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3-24-04

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Robin E. Barnum

Dear Sir:

Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449. Copies of the noted references are enclosed herewith. The relevancy of each of the references is noted on the enclosed Search Reports.

Since this IDS is being submitted before any Office Action, no fee is due.

Respectfully submitted,



Robert D. Marshall, Jr.
Attorney for Applicant
Reg. No. 28,527

Texas Instruments Incorporated
P.O. Box 655474, MS 3999
Dallas, TX 75265
(972) 917-5290

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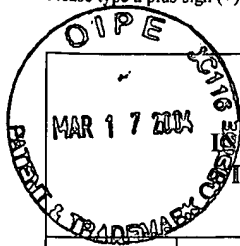


PTO/SB/08A (08-00)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet	1	of	1	Application Number	
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Complete If Known

Filing Date	November 9, 2001
First Named Inventor	Pochiraju Srinivas Rao
Group Art Unit	2853
Examiner Name	
Attorney Docket No.	TI-31068

U.S. PATENT DOCUMENTS

Exam. Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
	AA	6,115,131 A		Ralph E. Payne	9/5/00	
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		Office ³	Number ⁴	Kind Code ² (if known)				
	BA	EP	0 801 357 A		Canon KK	10/15/97		
	BB	WO	96 39680 A		Apple Computer	12/12/96		
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Exam. Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
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